



AMKASYN

Adapter AP-CI6 (Part-Nr. O780)

ACC – SubD 9pin / M12

(Top hat rail mounting)

Version: 2009/14

Part-No.: 201713

AMK

About this documentation

Name: PDK_201713_Adapter_AP-CI6_en

Use: Application examples and Pin Assignment of the AP-CI6 Adapter

What has changed:

Version	Change	Subject	Letter symbol
2007/14			
2008/36		first Flare version	BIs

Further Documentation:

Target group:

Representation agreement:

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- the device setup and application
- the type of malfunction, suspected cause of failure
- the diagnostic messages (error messages)

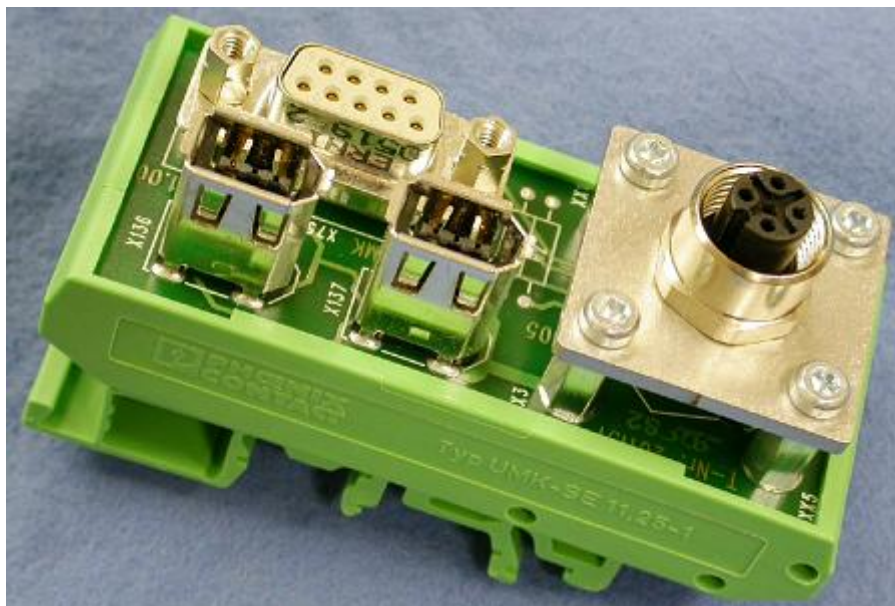
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1 Use



picture name: ZCH_Adapter_AP-CI6

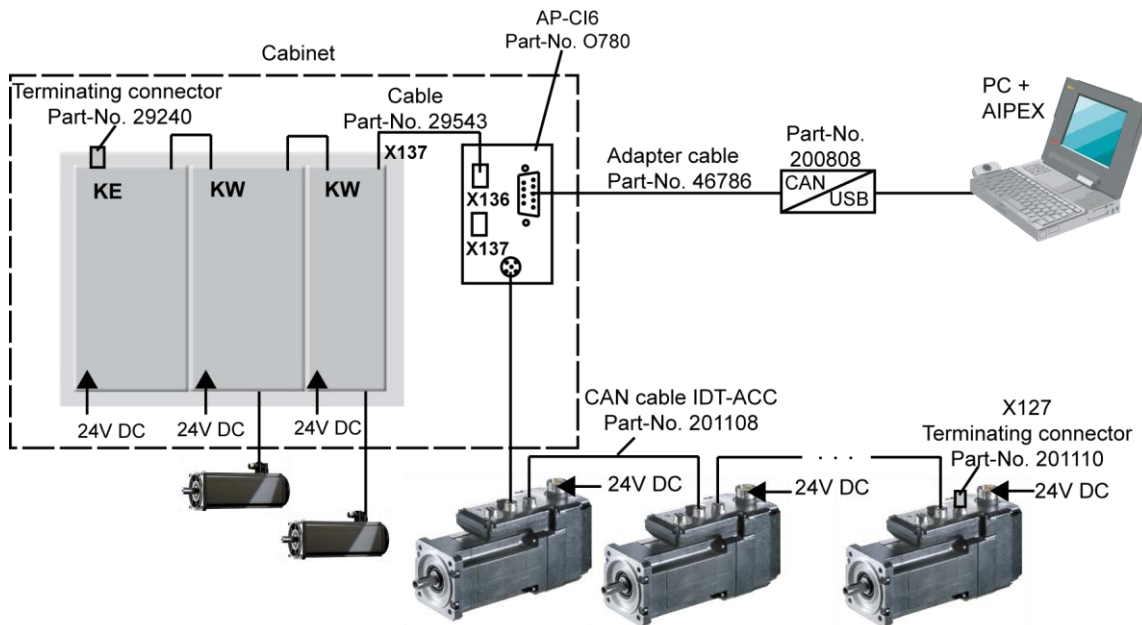
The USB-CAN bus interface converter interlinks the ACC-Bus interface (AMK CAN Communication) of the AMK devices with a USB / CAN inverter (AMK part-no.:200808). The connection is used for parameter setting and startup drive system with the AMK PC software AIPEX (AMK part-no.:46600) and for programming with the PC-software CoDeSys (AMK part-no.: 46430).

The top hat rail mounting allows fix mounting of the converter in the cabinet. Thus every time a running machine can be connected to a PC via ACC-Bus to get access to the drives (hotplug via X75 (9 pin D-SUB)).

The ACC-Bus interface is located on the top of the AMK controller card of the drive systems KU or KE/KW. or on the option card AS-FCT1 on the AMK controller.

2 Application examples

2.1 Example KE/KW and IDT



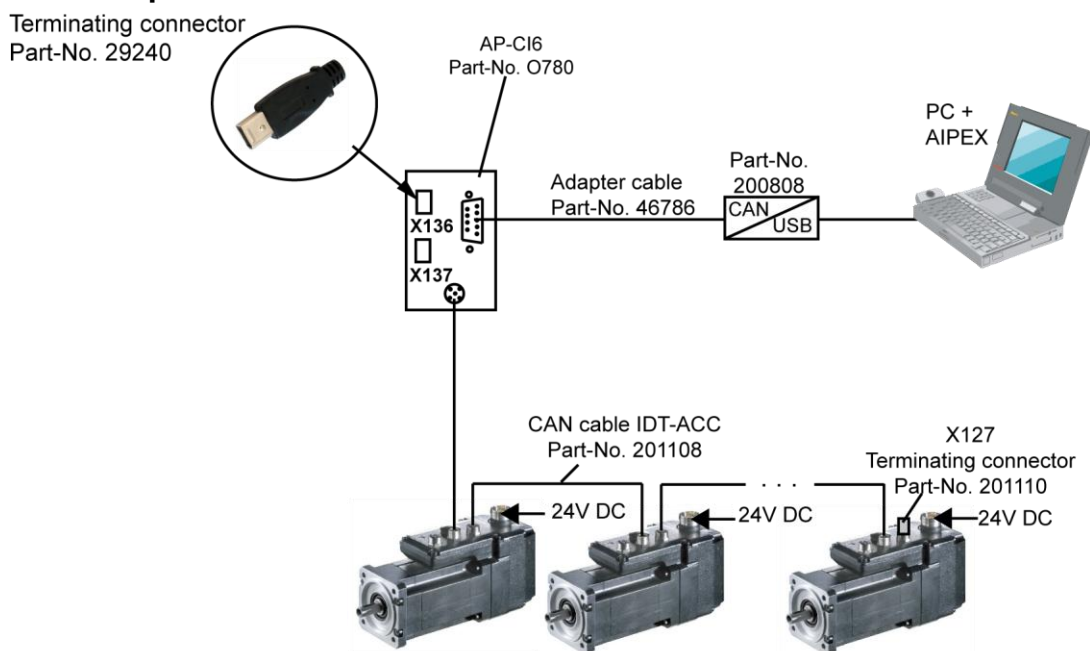
picture name: ZCH_IDT_Inbetriebnahme_1



The KU units will be connected as the same way as the KE/KW units.

The converter can also be used as portable tool e.g. for programming and setup of the KU-/KW-R03P controller card, if the serial interface is used by a modbus HMI.

2.2 Example IDT without KE/KW



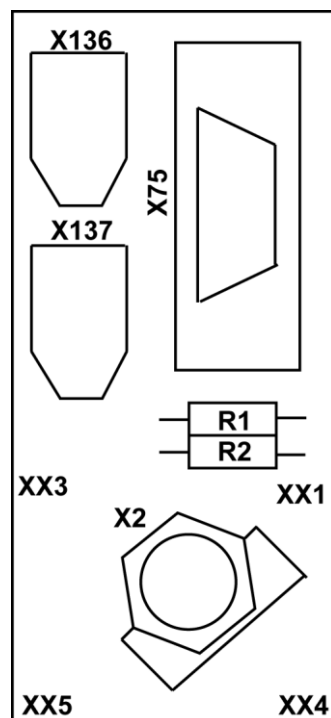
picture name: ZCH_IDT_Inbetriebnahme_ohneKEKW

3 Pin assignment X2, X75, X136, X137

SUB-D-9socket	X75 Signal
1	N.c.
2	CAN_L
3	GND
4	(SYNC_L)*
5	N.c.
6	N.c.
7	CAN_H
8	(SYNC_H)*
9	N.c.

IEEE1394 Firewire	X136 ACC-IN	X137 ACC-OUT
1	Opt. GND	Opt. GND
2	GND	GND
3	SYNC_H	CAN_H
4	SYNC_L	CAN_L
5	CAN_H	SYNC_H
6	CAN_L	SYNC_L

M12-socket	X2 Signal
1	GND/PE
2	SYNC_H
3	SYNC_L
4	CAN_H
5	CAN_L



picture name: ZCH_AP-CI6_Pinbelegung



If you complete R1 and R2 with two 0 Ohm resistances the hardware synchronization signal will be connect with the SUB-D-9 socket.

The interlink from adapter AP-CI6 to the ACC-bus must be at first or last ACC node. The adapter AP-CI6 is only allowed to connect to a ACC-bus at zero current.

4 Technical details

Dimension: L77 x B34 x H38 mm (without top hat rail)

Class of protection: IP 00

Top hat rail mounting

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