

AMK

AMKASYN

VARIABLE SPEED DRIVES

AMKASYN

Digital inverters in modular construction

Additional card for central module AZ

Programmable controller AZ-PS5-I with INTERBUS-S Interface

Important advice:

Touching of the electrical connections on the plug-in card must be avoided, otherwise electronic components could be destroyed through static discharge.

Take plug-in card directly out of packing and insert into option slot 4 in the AZ module without using force. Then secure with screws below the card grip.



Rights reserved to make technical changes

Programmable controller AZ-PS5-I

The option card AZ-PS5-I normally is plugged into slot 3 of central module AZ. Together with the INTERBUS-S interface 2 slots are occupied. It is secured in the front panel by 2 captive screws below the card grip against inadvertent loosening.

The option card AZ-PS5-I then must be assigned to slot 3 in ID32882 „Slot assignment“ in the basic system:

ID32882: xx xx 81 00 hex.
 00 instead of xx if slot 1 and 2 are free.
 If additional option cards are used in slot 1/2 the corresponding card codes must be entered instead of xx.

Technical data: INTEL Processor 80960JT100, 100MHz
 2 Mbyte Flash-ROM
 128 kbyte Boot-Flash
 512 kbyte RAM
 512 kbyte battery-backed RAM
 RS422 / RS485 interface (15-pole SUB-D)
 RS232 Debug interface (USB)
 Internal field bus interface

The AZ-PS5-I card is used as a programmable drive interface to solve tasks closely related to the drive. Data communication with the drive system takes place through the internal bus.

Binary and analog inputs/outputs, the INTERBUS-S interface and the AMK panel AB 202L can be used for process and user level communication.

The AZ-PS5-I is programmed in statement list (STL, similar STEP 5). Programming unit is a standard PC with the AMK PS programming software APROS. The instruction set contains statements for logic operations, counters and timers. Drive setpoint values (torque, speed, position) and parameter changes are commanded via AMK specific function blocks.

Among other things coordinated axis movements can be generated through Fast Functions with table interpolation.

The user PS program (max. 96 kB) and non volatile data blocks (max. 15 kB) are stored in the battery-backed RAM memory. The capacity of the Lithium battery is able to maintain the data for at least 5 years.

The storage life of a AZ-PS5-I card with stored PS program is limited up to a maximum of 5 years!

If the battery is removed, all stored data are lost!

Description of the display and operator elements at the AZ-PS5 front panel:

- L1** (green): Not used
L2 (red): CPU Error
ER (red): PS Error
During a malfunction (PS ERROR state) this LED is blinking with a flashing rate of 1 second.
SP (red): STOP
In PS STOP state this LED is on
LO (green): see below
RN (green): RUN
In normal operation (PS RUN state) this LED is reset after the process image „INPUTS“ is formed and set again before the process image „OUTPUTS“ is transmitted, i.e. with increasing PS cycle time the LED brightness is decreasing. In PS STOP state LED „RN“ is off.

Switch positions:

- SP:** Stop (notched position)
The cyclic program execution is interrupted.
RN: Run (notched position), **switch position for normal operation.**
Normal cyclic PS program execution.
RS: Reset (momentary contact)
PS RESET is initiated, then automatic PS START (state RUN)

Additional PS RESET (RS) Functions

PS RESET (RS) function 1: Erase of all data blocks in the in battery backed RAM.

PS RESET (RS) function 2: Erase of the actual PS project (and loading of the user PS project stored on the system EPROM, if existing).

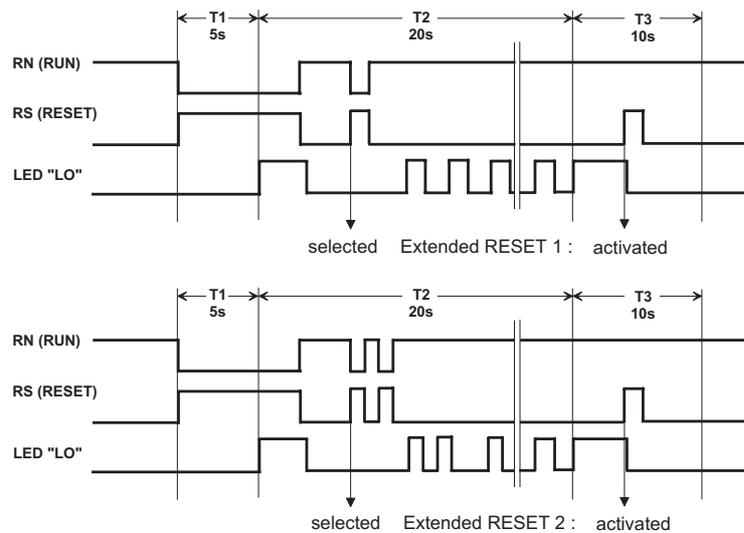
The RESET (RS) switch position must be pressed for more than 5s (T1) to initiate these extended functions. After this time the green LED „LO“ is turned on. Now the RESET switch must be released (back to RN position)

By a single operation of the RESET function 1 is selected, by a double operation extended PS RESET function 2 is selected.

LED „LO“ is reset, than extended function 1 is handshaked by a single flash of LED „LO“, extended function 2 by a double flash.

After T2 (20s) is elapsed LED „LO“ is constantly turned on for 10s (T3). The selected extended RESET function is activated by pressing RESET (RS) once more within this time.

A normal PS RESET and restart is initiated if RESET (RS) switch position is pressed for less than 5s or if the times T2/T3 are elapsed without selecting and activating one of the extended RESET functions via RESET (RS) switch.



Jumpers

The on-board jumpers have no meaning for the user.

Backup battery

The AZ-PS5 memory is battery-backed by a 3V Lithium battery Type CR2032.

With power on the battery voltage is monitored. If the battery voltage is too low a PS error message is output:

Error code 2816 „PS option error“, PS Error module: 120, Error-No: 1.

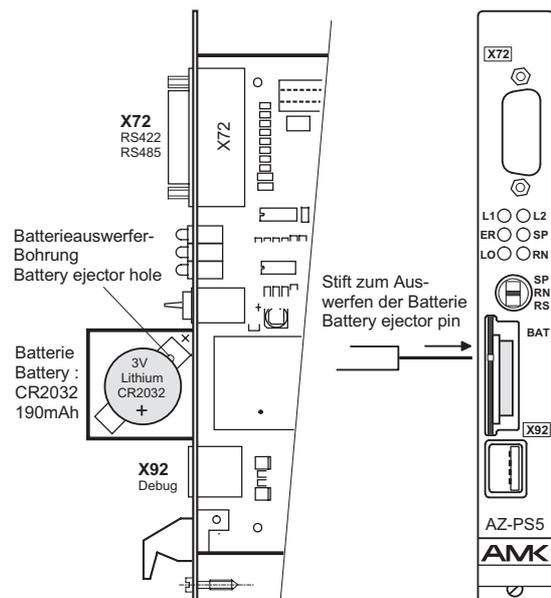
From this time the battery capacity is still sufficient to maintain the memory for 1 week! To avoid loss of data the battery must be exchanged immediately!

Battery change

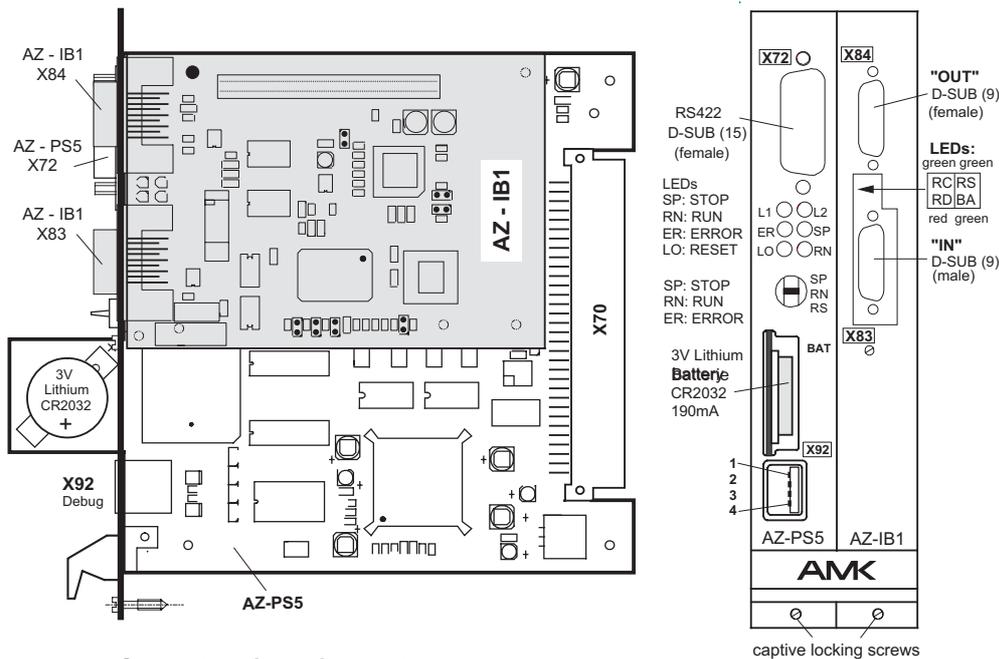
Battery change is only permissible with system POWER ON!

Battery Type:
3V Lithium CR2032

1. During battery change the power supply must be ON!
2. Carefully press out the old battery with a suitable ejector pin. Carefully press in the new battery into the holder. Pay attention to battery polarity: The battery designation (CR2032 +) must be visible. Keep the battery circumference absolutely clean, don't touch it!
3. Now Power OFF and ON again.



AZ-PS5-I component mounting diagram and front panel



Connector pin assignment:

AZ - IB1:

X83 (IB IN)

| Pin | Signal |
|-----|----------|
| 1 | DO1 |
| 2 | DI1 |
| 3 | Ground 1 |
| 4 | - |
| 5 | - |
| 6 | /DO1 |
| 7 | /DI1 |
| 8 | - |
| 9 | - |

X84 (IB OUT)

| Pin | Signal |
|-----|----------|
| 1 | DO2 |
| 2 | DI2 |
| 3 | Ground 2 |
| 4 | - |
| 5 | - |
| 6 | /DO2 |
| 7 | /DI2 |
| 8 | - |
| 9 | RBST |

LEDs (AZ - IB1):

IBS Reset inactive
 RC RS — SUP1 Reset inactive
 RD BA — IBS active
 Follower IB station not active

AZ-PS5 Serial interface: RS422 (RS485 capable)

X72 Connector pin assignment:

(15 pole female SUB-D)

| Pin | Signal | Line | Type | Meaning |
|-----|--------|------|-------|--------------------------------|
| 1 | PE | | | Shield |
| 2 | TxD- | O | RS422 | Transmit Data- |
| 3 | RxD- | I | RS422 | Receive Data- |
| 4 | RTS | O | RS422 | Request to Send+ |
| 5 | CTS | I | RS422 | Clear to Send+ |
| 6 | TxD- | O | RS422 | Contact for RS485: TxD- → RxD- |
| 7 | GND | | | Signal Ground |
| 8 | TxD | O | RS422 | Contact for RS485: TxD → RxD |
| 9 | GND | | | Signal Ground |
| 10 | 5P | | | 5 Volt supply |
| 11 | MP3 | I | TTL | Disable RS485 termination |
| 12 | TxD | O | RS422 | Transmit Data+ |
| 13 | RxD | I | RS422 | Receive Data+ |
| 14 | RTS- | O | RS422 | Request to Send- |
| 15 | CTS- | I | RS422 | Clear to Send- |

RS485 operation

For RS485 operation bus termination is required at the beginning and at the end of the transmission line. An on-board analog switch is provided to activate the bus termination resistors at the last bus station. For the remaining bus stations the bus termination must be disabled. For this in SUB-D connector X72 Pin 11 (MP3) must be connected to +5V (Pin 10 / 5P). Without this jumper (default) the bus termination is always active.

DEBUG interface X92

(4 pole, male USB plug connector,
AMK interface cable, part No.: 28265)

Only for AMK development / service use.

| Pin | Signal | Type | Meaning |
|-----|--------|--------|---------------|
| 1 | 5P | Power | 5 Volt supply |
| 2 | RxD | RS232 | Receive Data |
| 3 | TxD | RS232 | Transmit Data |
| 4 | GND | GND | Signal Ground |
| 5,6 | PE | Shield | shielding |

INTERBUS-S interface

Communication between the higher ranking controller (e.g. PLC) and the drive system (PS) via INTERBUS-S is made possible with option card AZ-PS5-I (AZ-PS5 with communication adapter AZ-IB1). For data exchange a maximum of 8 byte input and 8 byte output information is available.

The AZ-PS5-I also supports the "Peripherals Communication Protocol" (PCP).

Associated with option card AZ-PS5-I the INTERBUS-S interface is a long-distance bus slave connection:
INTERBUS-S ID code (bit 0...7) = 03: without PCP
INTERBUS-S ID code (bit 0...7) = F3: with PCP

In operating mode „Miroprocessor Access“ the INTERBUS protocol chip SUPI-3 is used in interrupt mode with 8 byte data width.

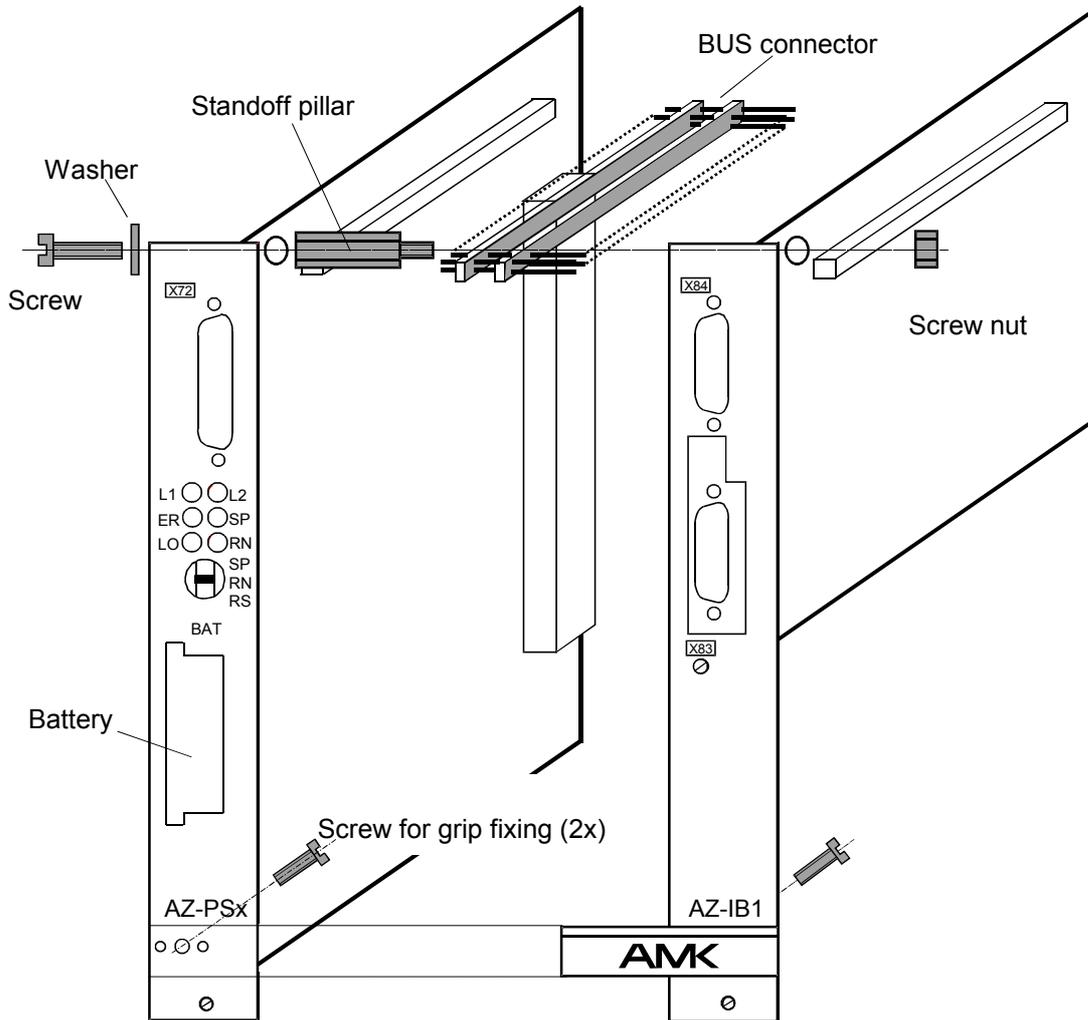
The following interrupt sources are activated: INTERBUS active
INTERBUS RESET
Data cycle valid

Via direct mapping of the INTERBUS-S data into the I/O address space of the AZ-PS5 the STL programmer gets access to the INTERBUS-S. In PCP mode data blocks can be written and read.

8 byte IN and 8 byte OUT information are provided in the process image of the AZ-PS5 for INTERBUS-S data. As a default setting the I/O addresses assigned to option slot 4 are used. The default setting for the INTERBUS-S interface can be modified through system data block DB 0 to adapt to the application demands (e.g. different INTERBUS-S data width, PCP selection, variable length of the PCP object "Data block [index 2001 hex]").

All information and data concerning the INTERBUS-S configuration are described in the PS documentation "AMKASYN PS Instruction set".

Assembly of AZ-PS5 and AZ-IB1 to AZ-PS5-I module (principle)



AZ-PS5 ESD-PROTECTION / INSTALLATION:

Please do not touch the electrical connections or the exposed contacts on the front or backside of the plug-in circuit boards. Static-electricity due to handling of the boards can destroy the boardlevel components. Please make sure the person handling the boards has proper PE-ground connection to reduce static-electricity.

Please insert the plug-in board directly from the packaging into slot 3 in the AZ-module without using force and secure the board with the captive screws underneath the card-holder.

Inappropriate handling of the board can lead to a short-circuit in the battery power supply, which could cause a loss of stored data in the user program.

- Never lay the board on a conductive surface (metal table top).
- Avoid touching the front or back side of the board.
- During the insertion of the plug-in board into the slot of the AZ-module it is imperative that the solder side of the board does not make contact with the frontcover of already inserted boards. If necessary remove the other board first before installing the AZ-PS5-I card.

Sequence for exchange procedure of the AZ-PS5-I card:

1. Make sure the AMKASYN-system is without power.
2. Remove front cover on AZ-module.
3. Remove external connections to the to be exchanged AZ-PS5 card (slot 3 and 4).
4. If existing: Remove external connections to the plug-in board in slot 2.
5. Unscrew the captive screws which hold the boards in place (slot 2, 3 and 4).
6. Remove the plug-in board from slot 2 by the card holder and lay it on a nonconductive surface (bubble wrap etc.).
7. Remove the AZ-PS5-I card by the card holder and lay on a nonconductive surface (bubble wrap etc.).
8. Take the new AZ-PS5-I card out of the packaging. Only handle it by the front card holder or by the front cover.
9. Insert this new AZ-PS5-I card into slot 3 in the AZ-module and secure it with the captive screws.
10. Replace the other option card again, if removed under 6.
11. Connect all external connections removed under 3. and 4. again and secure the wiring.
12. Download user PS program to AZ-PS5-I via programming software APROS (only if new card was inserted without PS program).